



11/25/96

## CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first class mail addressed to:  
Assistant Commissioner for Patents, Washington, D.C. 20231

on

11/18/96  
DateJudith C. Bares  
Judith C. Bares

## PATENT APPLICATION

Attorney Docket No. D/95473

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Marimont et al.**Application No.: **08/581,669**Group Art Unit: **2415**Filing Date: **12/29/1995**Examiner: **To be Assigned**For: **DYNAMIC COMPUTATION OF A LINE SEGMENT ARRANGEMENT USING FINITE PRECISION ARITHMETIC**INFORMATION DISCLOSURE STATEMENT  
TRANSMITTAL LETTER UNDER 37 CFR 1.97(b)Assistant Commissioner for Patents  
Washington, D. C. 20231

Sir:

Pursuant to 37 C.F.R. 1.56 and 1.97(b), the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. One copy of each listed reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

The accompanying Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits.

No certification or fee is required.

☐ A non-English language reference is enclosed. Per 37 CFR 1.98(a)(3) a copy of a foreign search report citing this reference or other concise explanation of relevance thereof is attached.

Respectfully,

  
Judith C. BaresSignature under 37 CFR 1.33 & 34  
Registration No. 35,824  
Telephone No. 415-812-4283☒ Attachment: PTO 1449 and copies of listed references

Rev.: 10/96



11/25/96

**PATENT APPLICATION**  
Attorney Docket No. D/95473**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:

Assistant Commissioner for Patents, Washington, D. C. 20231,  
on November 18, 1996.

11/18/96  
Judith C. Bares Date

Judith C. Bares  
Date Judith C. Bares

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application of: Marimont et al.

Art Unit: 2415

Appl. No.: 08/581,669

Examiner To be Assigned

Filed: 12/29/95

Title: **DYNAMIC COMPUTATION OF A LINE SEGMENT ARRANGEMENT  
USING FINITE PRECISION ARITHMETIC**Assistant Commissioner for Patents  
Washington, D.C. 20231**SECOND SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Sir:

In accordance with 37 CFR 1.97(a) and (b), Applicants submit herewith documents and information of which they are aware, which they believe may be material to the examination of this application, and in respect of which there may be a duty to disclose in accordance with 37 CFR 1.56.

The undersigned believes that this Statement is being filed before the mailing date of a first Office Action on the merits. Therefore, in accordance with 37 CFR 1.97(b), **no certification or fee is required.**

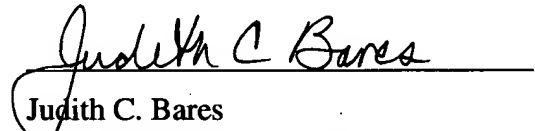
This Information Disclosure Statement is not intended to constitute an admission that any information referred to herein is "prior art" in relation to the claimed invention, unless specifically designated as such. In accordance with 37 CFR 1.97(g) and (h), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that the information submitted herewith is, or is considered to be, material to patentability as defined in 37 CFR 1.56(b).

The pertinent details of the enclosed document filed herewith for the subject patent application are set forth on the attached Form PTO-1449 (1 page). Document AP, the paper by Nahed and Peroche published in the January/February 1996 issue of the *Computer and Graphics* journal, discloses a model defined by a hierarchy and a superposition of planar maps which has been used to design a graphical interface for a knowledge-based system about networks. The formal definition of a planar map is disclosed at pg. 115, col. 2 in Sections 2.1 and 2.2. The data structure representation of planar maps is disclosed at pg. 116 in Section 2.3, and static and incremental construction of the data structures is described in Section 3, at pp. 116 - 118. The hierarchical structure of the planar map data structures is discussed in Section 5 beginning at pg. 119. All of these sections, as well as other material in this reference, may be relevant to the subject invention. It is believed that this reference is published after the filing date of the subject application.

While a brief discussion of the possible relevance of this reference has been provided herein, it is requested that the Examiner fully consider the reference in its entirety for its relevance to the subject patent application.

It is respectfully requested that the information submitted herein and the document submitted herewith be expressly considered during the examination and prosecution of this application, and that publicly-available documents be made of record in this application, and appear among the "References Cited" on any patent to issue therefrom. The undersigned respectfully requests that the enclosed copy of the Form PTO-1449 be initialed by the Examiner to indicate such consideration and that a copy thereof be returned to the applicant.

Respectfully submitted,

  
Judith C. Bares  
Attorney for Applicant(s)  
Registration No. 35,824  
Telephone: 415 - 812 - 4283

Palo Alto, California  
Date: November 18, 1996